

**REMARKS**

Favorable consideration and allowance of the claims of the present application are respectfully requested.


In the present Official Action, Claims 1-22 which are present in the above-captioned application have been subjected to a restriction under 35 U.S.C. §121 as follows:

- I. Claims 1-22, drawn to an apparatus including a microprocessor subsystem on SOC IC with multiple processor devices core communicating with bus devices and coupled to a memory, classified in Class 712, subclass 32; and
- II. Claims 23-30, drawn to a multiprocessor subsystem core in SOC IC with switch fabric, classified in Class 712, subclass 32.

As indicated, and in order to be fully responsive to the restriction requirement imposed in the present Official Action, applicants provisionally elect, without traverse, to prosecute the subject matter of Group I, i.e., Claims 1-22, and reserve the right to file one or more divisional applications directed to the non-elected subject matter in this application, i.e., Claims 23-30.

Wherefore, consideration and allowance of the claims of the present application are respectfully requested.

Respectfully submitted,

  
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